09898360 Notice of Allowability

Application No.	Applicant(s)	
09/898,360	SEALS ET AL.	
Examiner	Art Unit	
Alexander Eisen	2674	

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The MAILING DATE of this communication appearance and claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIOF the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this app or other appropriate communication GHTS. This application is subject to	olication. If not include will be mailed in due	ed course. THIS
1. This communication is responsive to Application filed on 0.	<u>3 July 2001</u> .		
2. X The allowed claim(s) is/are <u>1-6</u> .			
3. The drawings filed on 18 September 2002 are accepted by	the Examiner.	مجسر	
 4. Acknowledgment is made of a claim for foreign priority ur a) All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority documents have International Bureau (PCT Rule 17.2(a)). 	been received. been received in Application No		ition from the
* Certified copies not received:			
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONM—THIS-THREE-MONTH PERIOD IS NOT EXTENDABLE. 5. A SUBSTITUTE OATH OR DECLARATION must be submit to the submit of the submit to the submit of the submit to the submit of t	IENT of this application. itted. Note the attached EXAMINER'	'S AMENDMENT or N	
INFORMAL PATENT APPLICATION (PTO-152) which give	es reason(s) why the oath or declara	tion is deficient.	
 6. CORRECTED DRAWINGS (as "replacement sheets") mus (a) including changes required by the Notice of Draftspers 1) hereto or 2) to Paper No./Mail Date (b) including changes required by the attached Examiner's Paper No./Mail Date 	on's Patent Drawing Review (PTO-		
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in t			back) of
7. DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT	sit of BIOLOGICAL MATERIAL n	nust be submitted.	Note the
Attachment(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)	5. ☐ Notice of Informal P	(PTO-413),	O-152)
 3. ☑ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date 03/07/2001 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material 	Paper No./Mail Dat 18), 7. Examiner's Amendr 8. Examiner's Statement 9. Other	nent/Comment	owance
	8-00 Q1	Alexander Eisen	

Primary Examiner Art Unit: 2674

Application/Control Number: 09/898,360

Art Unit: 2674

DETAILED ACTION

Allowable Subject Matter

- 1. Claims 1-6 are allowed.
- 2. The following is an examiner's statement of reasons for allowance: none of the references, either singularly or in combination, teach or fairly suggest modification or combination with the cited below prior art, which is considered to be pertinent to the invention, so as to satisfy the combination of all the limitations of independent claim 1; none of the references have been found by the examiner that teach or suggest a video converter board for converting stroke/raster video formatted data Red, Green, Blue (RGB) video having a sync-ongreen (SOG) control signal formatted for a serial interface conforming to Electronic Industries Association (EIA) standards, the stroke/raster video data comprising X deflection signals, Y deflection signals and video-in signals, wherein the RGB video data is being synchronized by the sync-on-green (SOG) signal, the video converter board comprising an analog to digital front-end circuitry sampling at predetermined rate said X deflection, Y deflection and video-in signals and converting each sampled signal into a corresponding digital quantity; a video memory comprised of a plurality banks for storing each digital quantity; a memory control circuitry for sequentially selecting each of the plurality of the memory banks; a synchronization control circuitry for generating timing signals for the sync-on-green (SOG) signal and the memory addresses for reading and blanking; and a digital/analog converter output circuitry receiving in response to the synchronization control circuitry formatted RGB video data as well as the timing signals for the SOG signal and outputting the SOG signal along with the RGB video data in a manner conforming to the interface.

Application/Control Number: 09/898,360

Art Unit: 2674

Sharp et al., US 5,557,297, discloses a system for converting XY stroke signals (X and Y deflection signals) into RGB signals for driving a raster type display having analog-to-digital front-end circuitry sampling at predetermined rate the X deflection, Y deflection and video-in signals, and converting each sampled signal into a corresponding digital quantity; a video memory comprised of a plurality memory banks for storing each digital quantity; a memory control circuitry for sequentially selecting each of the plurality of the memory banks, and synchronization control circuitry. Sharp et al. does not disclose a control circuitry for generating timing signals for the sync-on-green (SOG) signal, and a digital-to-analog converter output circuitry receiving in response to the synchronization control circuitry formatted RGB video data as well as the timing signals for the SOG signal and outputting the SOG signal along with the RGB video data.

Tanner et al., US 6,496,160 B1, discloses a stroke to raster converter system including some component and function as those of the invention, but fails to provide for converting stroke signals into RGB plus SOG signals for driving a raster display.

Lee, US 6,064,443, discloses a method of driving raster type display utilizing SOG signal.

None of the above teach or suggest the combination of limitations as claimed by the invention.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Application/Control Number: 09/898,360

Art Unit: 2674

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Alexander Eisen whose telephone number is (703) 306-2988. The examiner can normally be reached on M-F (8:30-4:00).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bipin Shalwala can be reached on (703) 305-4938. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

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Alexander Eisen Primary Examiner Art Unit 2674

21 September 2004